

ABSTRACT OF THE DISCLOSURE

A power supply for a hybrid scavengerless development (HSD) image forming system which generates three voltages, a mag voltage, a donor voltage and a wire electrode voltage. The power supply outputs square waveforms for generating ~~the~~ toner clouds to maximize the voltage push-pull on ~~the~~ toner without increasing ~~the~~ peak voltage level. The power supply also generates asymmetric waveforms for ~~the~~ mag AC, donor AC and ~~wire~~ electrode wire voltage signals to avoid air breakdown at ~~the~~ a wire electrode to donor interface while allowing maximum use of ~~the~~ a voltage space. Finally, the power supply uses frequency modulation of the AC signals to suppress harmonic strobing of ~~the wire electrodes~~ electrode wires.